IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Confirmation No.: 6007 Kenichi ADACHI

Group Art Unit: Not Yet Assigned

Application No.: 10/589,380

Examiner: Not Yet Assinged

Filing Date: August 15, 2006

Attorney Docket No.: 545/73

For: Processor, Multiprocessor System, Processor

System, Information Processing Apparatus, and

Temperature Control Method

INFORMATION DISCLOSURE STATEMENT

VIA EFS

Mail Stop: Amendment Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant respectfully submits this Information Disclosure Statement, identification of the information, and copies thereof if required pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98 in order to comply with the duty of disclosure. The enclosed Form PTO SB/08A/B identifies the references of which Applicant is aware. The Examiner is respectfully requested to consider the information herein and make it of record in the subject application.

By checking this paragraph, this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b) within three months of the filing date of the application or before the application has received a substantive Office Action. Accordingly, no fee is necessary; however, should the Patent Office determine that a fee is due, the Commissioner is authorized to charge any such fee to Deposit Account No. 11-0223.

By checking this paragraph, this Information Disclosure Statement is being filed pursuant to
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Application No.: 10/589,380	Attorney Docket No.: 545/73								
By checking this paragraph, this	Information Disclosure Statement is being filed pursuant to								
37 C.F.R. § 1.97(d) after the mailing da	ate of a Final Action, a Notice of Allowance, or close of								
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this Information Disclosure Statement of office in a counterpart foreign applicated Information Disclosure Statement; or By checking this paragraph, the unit of this Information Disclosure Statement of counterpart foreign application, and, the motion of information contained in the	ndersigned states that each item of information contained in was first cited in a communication from a foreign patent tion not more than three months prior the filing of this undersigned states that no item of information contained in was cited in a communication from a foreign patent in a undersigned, after making reasonable inquiry, believes that his Information Disclosure Statement was known to any 56(c) more than three months prior to the filing of this								
Dated: December 18, 2006	Respectfully submitted, By: s/Matthew B. Dernier/ Matthew B. Dernier Registration No. 40,989 KAPLAN GILMAN GIBSON & DERNIER LLP 900 Route 9 North Woodbridge, New Jersey 07095 Telephone (732) 634-7634 Attorneys for Applicant								

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Attorney Docket Number

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Examiner Cite Initials* No.1		Document Number	Publication Date		Name of Patent oplicant of Cited D		Pages, Columns, Lines, Where Relevant Passages or Relevant		
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NON PATENT LITERATURE DOCUMENTS										
Examiner Initials*	No. Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.									
	CA	International Preliminary Report on Patentability to corresponding PCT/JP2004/019208, dated 11/29/2006, 5 pgs.	✓							
	СВ	BENINI et al.; "Reducing Power Consumption of Dedicated Processors Through Instruction Set Encoding;" VLSI, 1988, Proceedings of 8th Great Lakes Symposium on IEEE, pp. 8-12.	✓							
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